

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application

Applicant(s): A.Q. Khan et al.
Case: 4-14-28
Serial No.: 10/722,933
Filing Date: November 26, 2003
Group: 2619
Examiner: Brian T. O'Connor

Patent No.: 7,477,636
Issued: January 13, 2009

Title: Processor with Scheduler Architecture Supporting Multiple
Distinct Scheduling Algorithms

**REQUEST FOR CERTIFICATE OF CORRECTION OF
PATENT FOR PTO MISTAKE UNDER 37 C.F.R. 1.322(a)**

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Attn: Certificate of Corrections Branch

Sir:

In accordance with 37 C.F.R. 1.322, Applicants hereby request the issuance of a Certificate of Correction with respect to errors in the above-identified patent.

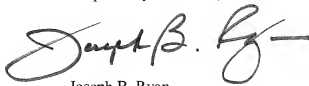
The errors are set forth on the proposed Certificate of Correction (PTO-1050) submitted herewith.

The errors are PTO mistakes, namely, typographical errors.

Accordingly, this certificate should be issued pursuant to 37 C.F.R. §1.322(a).

It is believed that there is no additional fee due in conjunction with this Certificate of Correction. In the event of any non-payment or improper payment of a required fee, the Commissioner is hereby authorized to charge or to credit **Ryan, Mason & Lewis, LLP Deposit Account No. 50-0762** as required to correct the error.

Respectfully submitted,

A handwritten signature in black ink, appearing to read "Joseph B. Ryan", followed by a horizontal line.

Date: January 22, 2009

Joseph B. Ryan
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**UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION**

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APPLICATION NO.: 10/722,933

ISSUE DATE : January 13, 2009

INVENTOR(S) : A.Q. Khan et al.

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In the specification:

Column 7, line 13, please delete "to" and insert --can--.

In the claims:

Claim 1, column 9, line 36, please delete "scheduler" and insert --scheduling circuitry--.

Claim 1, column 9, line 47, please delete "scheduler" and insert --scheduling circuitry--.

MAILING ADDRESS OF SENDER (Please do not use customer number below):

Ryan, Mason & Lewis, LLP
90 Forest Avenue
Locust Valley, New York 11560

This collection of information is required by 37 CFR 1.322, 1.323, and 1.324. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1.0 hour to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Attention Certificate of Corrections Branch, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

If you need assistance in completing the form, call 1-800-PTO-9199 and select option 2.